Hybrid Data-Driven Based HVdc Ancillary Control for Multiple Frequency Data Attacks

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Abstract—The high voltage direct current (HVdc) intertie has been applied to provide ancillary-services for ac grids, utilizing the real-time feedback from phasor measurement units (PMUs). However, PMU data communication is vulnerable to false data injection attacks (FDIA) due to protocol defects, thus the HVdc ancillary control and system stability will be threatened. To address this issue, this article proposes a novel HVdc control strategy based on a hybrid data-driven (HDD) methodology. The HDD methodology is first proposed to detect the types and duration time of multiple frequency attacks. Specifically, the Hilbert Huang transform (HHT) is used to decompose the frequency data, using variational mode decomposition instead of the traditional empirical mode decomposition, to extract data features. Second, a multikernel support vector machine is proposed to classify the attacked data based on the designed distinctive features from HHT. Meanwhile, the attacking duration time is decided using an unsupervised technique. Third, an HDD-based HVdc ancillary control strategy is established to eliminate the effect of FDIAs on the HVdc frequency response. Comprehensive experiments of HDD-based HVdc ancillary controls under different FDIAs suggest that the proposed HDD could fast and accurately classify the FDIAs, and the HDD-based HVdc ancillary control strategy could significantly suppress the impact of the FDIAs.

Manuscript received July 24, 2020; revised December 5, 2020 and February 1, 2021; accepted February 23, 2021. Date of publication March 3, 2021; date of current version August 20, 2021. This work was supported in part by the Engineering Research Center Program of the National Science Foundation, DOE under NSF Award No. EEC-1041877, in part by CURENT Industry Partnership Program, and in part by the Postgraduate Scientific Research Innovation Project of Hunan Province under Grant CX20200426. Paper no. TII-20-3561. (*Corresponding author: Kaiqi Sun.*)

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Color versions of one or more figures in this article are available at https://doi.org/10.1109/TII.2021.3063270.

Digital Object Identifier 10.1109/TII.2021.3063270

Index Terms—False data injection attack (FDIA), high voltage direct current (HVdc) ancillary control, hybrid datadriven (HDD), multikernel support vector machine (MSVM).

I. INTRODUCTION

H IGH voltage direct current (HVdc) systems have been successfully adopted as a solution to transmit bulk power over long distances and interconnect independent asynchronous power grids [1]. With the development of voltage source converter (VSC) technology, the functionality of the HVdc intertie has been significantly improved [2], [3].

An HVdc intertie has high controllability because it allows the independent control of active and reactive power, providing a quick dynamic response to various system disturbances. Therefore, the system configuration of HVdc intertie will result in benefits including reliability and ancillary-services [4]. In the result of interconnection seam study, the ancillary-services, including the frequency-response sharing, inertial response, and damping oscillation control, can provide 25% of the economic benefits using the HVdc intertie [4], [5]. Moreover, such economic benefits brought by the HVdc intertie assisted ancillary-services for ac grids are believed to offset the cost to construct the HVdc network [6].

Some power companies have tried applying the conventional HVdc interties for ancillary controls [7]–[9]. Specifically, the HVdc intertie utilizes real-time phasor measurement units (PMUs) feedback to construct a supplemental commanded power signal for the HVdc ancillary control. Based on the synchronization characteristic of the PMUs, in China, a wide-area adaptive damping control system through the modulations of multiple HVdc interties is developed by the china southern power grid [8]. In North America, a damping controller (DCON) utilizes a control scheme and real-time PMUs data to damp interarea oscillations by modulating the power, and has been implemented on the pacific dc intertie [9].

The performance of the ancillary service provided by the HVdc intertie is highly relevant to the accuracy of PMUs [10]. However, due to the fast power regulation capability of the HVdc intertie (power regulation rate could be up to 200 MW/s), the frequency deviation caused by false data injection attacks (FDIAs) may cause large power flow change of HVdc interties. For example, the impact of three types of cyberattacks on the HVdc transmission-based oscillation damping control was

1551-3203 © 2021 IEEE. Personal use is permitted, but republication/redistribution requires IEEE permission. See https://www.ieee.org/publications/rights/index.html for more information. studied, including the timing attack, replay attack, and FDIAs in [11]. The effect of attacks on the dynamic voltage stability and switching power losses and the vulnerabilities of the HVdc system is studied in work [12]. In the latest work [13], the vulnerability and impact of the hybrid ac/HVdc grid with virtual inertia to the FDIAs is also explored. The FDIA on the PMUs for the HVdc ancillary control may lead to the sever contingency in a system that is configured with HVdc interties [14]. Nowadays, with the growing probability of the FDIAs, the security of the PMU configured in the HVdc interties for ancillary control becomes a potential challenge to the system safety operation.

FDIA is a common type of data attack, which can happen on the communication and database levels [15]. Compared with bad data, the cyberattack is caused by the vulnerability of the network or communication protocol being exploited by the attacker [16]. However, most of the bad data are isolated and not in sequence [17]. And the number of contiguous bad data may no more than three points [18]. Based on the difference, bad data could be detected using some outlier detection methods or by setting the threshold. However, the FDIAs are different from bad data from another two aspects. 1) The attacked data may have no outliers. 2) The duration and amplitude of the attack can be maliciously modified to avoid outlier detection. Therefore, cyberattack can deceive the bad data detection [19]. For example, the synchrophasor data gradually changes in the form of a ramp, making it difficult to be found and managed [20]. To detect these FDIAs from PMU data, some research works have been conducted, which can mainly be categorized as model-driven and data-driven methods [15]. The model-driven method aims to establish a quasi-static or dynamic model to simulate power system parameters. Based on the real-time measurements, the weighted least squares and Kalman filter methods are introduced to detect the FDIA through the state estimation [21]. However, detection depends on the estimation method and system parameters. It means that the model-driven method may fail under an unknown model structure.

To address the limitations of the model-driven methods, some data-driven methods are proposed to detect FDIAs utilizing a data dependent and model-free manner. For example, the feed-forward artificial neural network (FANN) is developed to identify the spectrum of electric network frequency in [22]. Based on the study in [22], more than two methods are designed to distinguish data spoofing attacks including the multigrained cascade forest and random forest classification methods [23]. However, this article only considers the replacement of the frequency signal, which restricts its adaptability. Next, a discordant element approach is proposed to detect the FDIA in the dc microgrids [24]. Three attack numerical methods are simulated and verified. The above methods realize the identification of the attack, but the duration time of FDIA cannot be identified, which is an important control signal for the HVdc ancillary control. Therefore, using these methods, the performance of the HVdc ancillary control may be deteriorated.

To address these issues, this article proposes a hybrid datadriven (HDD) based HVdc ancillary control to improve the control performance of the HVdc interties under FDIAs. The contributions of this article are summarized as follows.



Fig. 1. Proposed HDD-based cyberattack detection framework.

- To achieve automatic identification of multiple frequency attacks, the Hilbert Huang transform (HHT) is utilized to decompose the measurements into the band-limited mode functions (BMFs) with variational mode decomposition (VMD). Next, the Multikernel support vector machine (MSVM) is proposed to construct a novel form of kernels and automatically classifying different FDIAs.
- 2) Using the anomalous characteristics of attack data, the fast unsupervised learning method k-Means is used to obtain the duration time. An HDD framework is also proposed combined with the HHT, MSVM and k-Means. Instead of targeting a specific attack, features of multiple frequency data are fused and identified.
- 3) Benefiting from the fast classifying of FDIAs, an HDDbased HVdc ancillary control strategy is proposed to reduce the effect of the FDIAs on the HVdc intertie. Then, the system operation stability under FDIAs can then be guaranteed.

The rest of the article is organized as follows. The HDD detection framework of the multiple frequency attack is introduced in Section II. The steps for constructing the HDD are proposed in Section III. Then, the duration time detection is shown in Section IV. The HVdc ancillary control is presented in Section V. Different experiment results are shown in Section VI. Finally, Section VII concludes this article.

II. DATA-DRIVEN BASED FDIA DETECTION FRAMEWORK

To accurately reduce the effect of the FDIA on the HVdc intertie, the cyberattack and duration time of the frequency data are detected first. Here, a novel hybrid data-driven base method with two objectives is proposed including classifying the attack and detecting its duration time, which including the start and end time. Denoting the measurement frequency data from the HVdc intertie as x(n), the framework of the HDD cyberattack detection method is shown in Fig. 1. It consists of the following steps.

- 1) Feature extraction: The HHT is performed to decompose data x(n) and extract the features from the decomposition results.
- 2) Identification of FDIAs: Using the extracted features, a multikernel SVM method is proposed to classify different types of attacks and labeled as L_A .
- Duration time detection: An unsupervised clustering method is used to identify the duration time of the attack. Combined with the identification results in the second

TABLE I FORMULATION OF ATTACK VECTORS

Different FDIAs (k_d)	Numerical models (k_d)
Ramping attack (A1)	$x(n) = x'(n) \pm \lambda_{ramp} t \Delta u(t)$
Data exchange attack (A2)	$x(n) = x'(n)(1 - \Delta u(t)) + x_1(n)\Delta u(t)$
Data loss attack (A3)	$x(n) = x'(n)(1 - \Delta u(t)) + f_{base}\Delta u(t)$
Scale attack (A4)	$x(n) = x'(n)(1 \pm \lambda_{scale} \Delta u(t))$
False oscillation attack (A5)	$x(n) = x'(n)(1 - \Delta u(t)) + \lambda_{osc}\Delta u(t)$

The u(t) is the heaviside step function. $\Delta u(t) = u(t - t_1) - u(t - t_2), 0 < t_2 - t_1 < S_t$, where the t_1 and t_2 are the attack time. S_t is the sampling time of each FDIA. $f_{\text{base}} = 60$ Hz, and different $\lambda_x = \{\lambda_{\text{ramp}}, \lambda_{\text{scale}}\}$ is the magnitude of attack data. λ_{osc} is the false oscillation data. $x_1(n)$ denotes the data from other measurement units.



Fig. 2. Five types of FDIAs.

step, the accuracy of duration time is optimized by excluding the normal frequency data. Finally, the detection results are fed to the HVdc control terminal.

In this article, five types of cyberattacks are considered including ramping attack (A1), data exchange attack (A2), data loss attack (A3), scale attack (A4), and false oscillation attack (A5). The numerical model and principle of these attacks are listed in Table I based on the definition in [20], [25]. The shapes of the attacks are shown in Fig. 2. Specifically, for the ramping attack, a gradual trend item is added to the measurement data x'(n). And the magnitude of the attack data can be controlled by λ_{ramp} [20]. And it is challenging to distinguish between normal and A2 data as shown in Fig. 2(a) and (c) due to their similar shape. For the A3 and A4, it shows that higher or lower values are modified compared with the original measurement value. And the difference between A3 and A4 is that the A3 data are lost so the data $f_{\text{base}} = 60 \text{ Hz}$ is compensated [25]. During $t_1 =$ 0.7 s and $t_2 = 1.42$ s, it is clearly found that a false oscillation is added in A5, which may cause false control actions.

The duration and length of this FDIA can be modified arbitrarily. To bypass the bad data detection, the parameters of different FDIAs should meet certain mathematical conditions. Usually, the 3σ criteria can be used to describe the characteristics of bad data [18]. The μ is the mean value and σ is the standard deviation of the frequency data. If there is bad data, the bad data might be outside the range ($\mu - 3\sigma, \mu + 3\sigma$) [18]. According to this criterion, if the cyberattack would like to bypass the bad data detection, suitable threshold parameters and conditions can be set so that the characteristics of attacked data is located in between ($\mu - 3\sigma, \mu + 3\sigma$).

Take the actual two-day measurement data from three PMUs (WECC system) as an example, the μ and σ are 60, 60.0032, 59.9998 and 0.0138, 0.0139, 0.0139 Hz, respectively. The mean values of μ and σ can be set to 60.001 and 0.01386 Hz, respectively. This means that the measurement frequency value outside the (59.95942, 60.04258) Hz can be treated as bad data.

For the ramping attack, the magnitude $\lambda_{ramp} t$ will determine whether the A1 can pass the bad data detection. Here, the x'(n)is set to 60 Hz, the $\lambda_{ramp}t$ should be satisfied $\lambda_{ramp}t + x'(n) \in$ (59.95942, 60.04258) Hz, and therefore the $\lambda_{ramp}t$ be located between -0.04258 and 0.04058 Hz. For the data exchange attack, the data is similar to the raw data, thus the magnitude will not exceed the range of $(\mu - 3\sigma, \mu + 3\sigma)$. For the data loss attack, the $f_{\text{base}} = 60 \text{ Hz}$ is compensated for A3. Obviously, this data range is the normal range. For the scaling attack, the λ_{scale} determines the magnitude and it should be satisfied $x'(n)(1 \pm \lambda_{\text{scale}}) \in$ (59.95942, 60.04258)Hz, and therefore λ_{scale} can be located in (-0.04258, 0.04058) mathematically. Otherwise, the A4 can be detected by the bad data detection. For the false oscillation attack, it also will bypass the bad data detection only if the amplitude and duration of the false oscillation coincide with the real event. The attackers can collect some historical events to forge a false oscillation attack.

III. IDENTIFICATION OF MULTIPLE FREQUENCY DATA

A. Signal Decomposition for FDIA Data

The first step to identify the cyberattack is to extract the deterministic features. The HHT is a nonlinear signal processing method that includes EMD and Hilbert transform (HT). It is suitable for processing nonlinear signals. Compared with the S transform [26], the HHT consumes less calculation time because the S transform needs to calculate multiple FFTs. However, the empirical mode decomposition (EMD) is prone to fall into modal aliasing, which leads to spectrum overlapping of frequency data. Then the ensemble EMD (EEMD) is proposed as an improved method based on EMD. However, EEMD usually integrates a large number of EMD calculation results and therefore increases the amount of calculation. To mitigate this problem, a frequency bandwidth decomposition method named VMD is used to replace the EMD process during the calculation of HHT. Compared with the EMD, the modal aliasing and endpoint effect problem of VMD can be mitigated when decomposing a nonlinear signal [27].

The purpose of VMD is to decompose the frequency data as x(n) into a combination of different BMFs $b_t(n)$ [27]. Each BMFs has a center frequency w_t , where the t denotes the number of BMFs. The primary principle of VMD is to solve a constrained variational problem, which can be expressed as

$$\min_{\{b_t\},\{\omega_t\}} \left\{ \sum_t \left\| \frac{\partial}{\partial_t} \left[\left(\delta(t) + \frac{j}{\pi n} \right) * b_t(n) \right] \exp^{-j\omega_t n} \right\|_2^2 \right\}$$

s.t. $\sum_t b_t(n) = x(n)$ (1)

To address the variational problem, the quadratic penalty term and Lagrange multiplier are introduced. Using the alternative direction method of multiplier (ADMM), the center frequency w_t and BMFs $b_t(n)$ can be constantly updated. Then the output of VMD can be expressed as

$$x(n) = \sum_{1}^{t-1} b_t(n) + b_{tr}(n)$$
(2)

where the $b_{tr}(n)$ is the residual mode component. Commonly, the $b_{tr}(n)$ represents the changing trend of the frequency data.

After obtaining the BMFs of FDIA, the physical parameters including the magnitude and frequency of each BMFs can be calculated according to the HT. Specifically, the BMFs is transformed into an analytic function by adding the imaginary part. This analytic function $H_b(n)$ can then be obtained as

$$H_b(n) = b_t(n) + j\bar{b}_t(n)$$

$$b_t(n) + j\frac{c_a}{\pi} \int_{-\infty}^{+\infty} \frac{b_t(\tau)}{n-\tau} d\tau$$
 (3)

where the c_a indicates the Cauchy principal value, $b_t(n)$ is the imaginary part of $b_t(n)$. Thereafter, the magnitude, phase and frequency of each BMFs can be derived from (3).

B. Feature Extraction

Once the BMFs of frequency data are decomposed, the next step is to extract the features so that the FDIA data can be easily detected. As can be seen from HHT, the sources of features can be inferred from the results of VMD and HT. The motivation of feature selection is to extract as many types of features as possible and avoid increasing the amount of calculation. The initial feature selection process is performed to filter out some redundant and low-precision features.

Accordingly, four distinctive features $F_1 - F_4$ are designed from HHT. These four features can be divided into two groups including two frequency domains and another two statistical features. The first F_1 is the primary frequency point of each BMFs which is derived from the instantaneous frequency [28]. The computation of the instantaneous frequency can be referred to [29]. The second F_2 is the amplitude spectrum of frequency data with the trend term removed. The spatial and temporal characteristics of synchronized data are different if the measurement data comes from different regions [23]. Therefore, the frequency properties will be changed if the data is attacked. The definition of F_1 [29] and F_2 are as follows:

$$F_1 = \frac{1}{2\pi n} \sum_n \frac{\mathrm{d}}{\mathrm{d}n} \arctan \frac{b_t(n)}{b_t(n)} \tag{4}$$

$$F_2 = \sum_{n=0}^{N-1} \left(x(n) - b_{tr}(n) \right) e^{(-i2\pi kn)/N}, k = 0, \dots, N-1$$
(5)

where N is the length of the frequency data x(n). In F_2 , the trend item $b_{tr}(n)$ is removed from x(n). The reason is that some attackers will replace the frequency data between multiple PMUs, while retaining the synchronous changes of the data. In this case, the recognition accuracy will be interfered if the trend item is retained.

Another two statistical features are the kurtosis (F_3) and envelope entropy (F_4) . The definition of F_3 and F_4 can be expressed as [30]

$$F_3 = \mathbf{E}\left[\left(\frac{b_t(n) - \mu_b}{\sigma_b}\right)^4\right] \tag{6}$$

where u_b is the mean value of each BMF, and σ_b is the standard deviation of BMF. The F_3 describes the shape of the BMFs, thus it has the potential to identify different attacks.

The definition of F_4 is [31]

$$F_4 = \sum_{j=1}^{s_t} \left(a(j) / \sum_{j=1}^{s_t} a(j) \right) \log \left(a(j) / \sum_{j=1}^{s_t} a(j) \right)$$
(7)

where a(j) denotes the envelope of the Hilbert transfer of $b_t(n)$, the s_t is the length of each BMF. The purpose of using envelope entropy is to measure the signal irregularity such as sparse characteristics.

C. Frequency Data Identification Using MSVM

To improve the detection performance, the SVM is used as the primary classification method which is a supervised process. Generally, the form of kernel function is relatively simple and fixed because one kernel function is used to process the input vector. Hence, SVM is difficult to apply to identify complex FDIAs, especially when the system is multi-input. To solve this limitation, a multikernel SVM is proposed to improve its FDIA detection capabilities.

Given the feature dataset $D = (F_d, y_x), d = 1, 2, 3, 4$, where the y_x is the label of F_d . The motivation of MSVM is to learn a hyperplane to separate the F_d . In MSVM, the purpose of the kernel function is to map the F_d to different hyperplanes. These indistinguishable input vectors can then be separated from each other after mapping. Some most commonly used kernels are the linear kernel, polynomial kernel, sigmoid kernel, and radial basis function (RBF) kernel. For instance, the RBF can be defined as

$$k_r(x_i, x_j) = exp\left(-\frac{\|x_i - x_j\|_2^2}{2\sigma_k^2}\right)$$
(8)

where $x_i, x_j \in x(n), \sigma_k$ indicates the width kernel parameter for adjusting the kernel shape.

In SVM, only a single kernel and a fixed parameter is used which limits the learning ability when processing the cyberattack data. However, not all features contain equal effective information. Considering the unequal information for different features, a multikernel methodology is proposed in MSVM, which the definition can be expressed as

$$k_m(x_i, x_j) = \sum_{1}^{d} w_d k_d(x_{F_d}^i, x_{F_d}^j)$$

s.t. $\sum_{1}^{d} w_d = 1, d = 1, 2, 3, 4$ (9)

where the $k_d(x_{F_d}^i, x_{F_d}^j)$ denotes corresponding kernel functions of input feature $F_1 - F_4$. w_d denotes the weight of kernel k_d , it is used to adjust the proportion of different features. Basically, the k_m is a linear combination of multiple kernel functions. Different from the SVM, multiple kernels are used to increase flexibility. Meanwhile, each input feature of FDIA can be mapped by a single kernel function. Therefore, it is expected to obtain four suitable kernel functions and parameters for $F_1 - F_4$.

After that, an objective function is established to learn the decision boundary Θ : $\omega^T k_m(x_i, x_j) + b = 0$, which can be written as [32]

$$\min(\omega, \xi_i) = \frac{1}{2} \|\omega\|^2 + C \sum_{i=1}^n \xi_i$$
 (10)

$$\begin{cases} \text{s.t. } y_x(\boldsymbol{\omega}, k_m(x_i, x_j)) + b) \ge 1 - \xi_i \\ \xi_i \ge 0, \ i = 1, \dots, n \end{cases}$$
(11)

where the ω and b are the weight and bias value of learning decision boundary, respectively, ξ_i is the slack variable, C denotes the regularization parameter.

This objective function can be solved by introducing the Lagrange multipliers. The labels of unknown attacked frequency data can be predicted (L_A) utilizing the decision boundary. To achieve sufficient performance, the parameters of MSVM (such as C, σ_k) need to be tuned during the training process.

IV. DURATION TIME DETECTION FOR FDIA DATA

In the previous step, when the attack just ends, the frequency data may still be marked as an attack signal by MSVM because this data still contains a small piece of tampered data. Therefore, the detection of duration time helps to improve the real-time control in HVdc intertie once the attack disappears.

The duration time detection of false frequency injection consists of the start and end time detection. Essentially, the duration time detection is a binary classification, where each attack data point is divided into normal and abnormal data. Here, the unsupervised k-Means cluster method is used to separate FDIA data. The advantage of k-Means is that its detection speed is fast, and the number of clustering categories can be specified.

The objective of k-Means is to partition the frequency data into η clusters. For each cluster, there is an adaptively changing cluster centroid. Denoted the cluster subsets and corresponding cluster centroid as C_i and c_i , respectively. The x_n indicates the *n*th data point of C_i . Typically, the determination of the cluster center includes the following steps [33].

1) Step 1: Initialize η cluster centroid c_i using the random method. The number of clusters η is set to 2, which represents the normal and attacked clusters.

- Step 2: For the cluster center c_i, select the x_n and determine which c_i is closest to x_n. These x_n close to c_i are regarded as a cluster C_i.
- 3) Step 3: Calculate the mean value of each cluster C_i in step 2, and then move the cluster center c_i to the location of the mean value.
- Step 4: Repeat steps 1 to 3 until the cluster center c_i no longer changes.

After that, the frequency data point in one cluster C_i belongs to the same cluster. For example, if the x_n in C_1 is the normal cluster, then the rest of the data in C_2 means the attacked data. The duration time can then be obtained by counting the location and length of the attacked data. In the interval of FDIA, multiple consecutive detected attack data points are identified as the beginning and the end of FDIA, which are denoted as t_f and t_e . The t_e means that HVdc can reuse the measured frequency data as soon as the attack disappears.

To evaluate the detection effect of the duration time, a mean accuracy (MA) evaluation index is proposed as

$$MA = \frac{1}{\eta} \sum_{i} \frac{x_i}{L_x}, \, i = 1, 2$$
 (12)

where x_i denotes the number of correctly identified data points in *i*th cluster, L_x denotes the length of the frequency data.

V. HDD-BASED HVDC ANCILLARY CONTROL

With the proposed HDD, the types and duration time of the attacked frequency data could be obtained. It provides an opportunity for the HVdc intertie operator to classify the events with a small delay. Then the preventive control strategies can be adopted to reduce the influence caused by cyberattack on HVdc ancillary control.

Here, an HDD-based HVdc ancillary control is proposed for suppressing the effect of the FDIA to the HVdc intertie. The HDD-based HVdc ancillary control consists of two control functions: detection-period ancillary control function (DACF) and cyberattack response control function (CRCF).

The objective of the proposed DACF is to provide an appropriate ancillary control to respond to the system disturbance in a certain while keeping its response capacity for avoiding potential cyberattack. The control diagram of the DACF is shown in Fig. 3.

As shown in Fig. 3, when the frequency deviation $(f_{\rm ref} - f_{\rm local}(f_{\rm remote}))$ is over the $f_{\rm deadband}$, the DACF is activated. The DACF generates a $\Delta P_{\rm dc}$ by calculating the deviation between the nominal and operating frequency. Then $\Delta P_{\rm dc}$ is sent to the power control terminal to adjust the power flow on the HVdc intertie. The working period of the DACF is from the beginning of the event until the HDD detects the type and duration time of the cyberattack. Different from the conventional frequency response control, the droop coefficient K_f in the DACF adopts a smaller value. Although this K_f selection may lose part of the frequency response capability of the HVdc intertie at the beginning of the event but could avoid the significant influence caused by cyberattack.

After that, the ancillary control of the HVdc intertie is achieved through the proposed CRCF. The objective of CRCF is



Fig. 3. Control diagram of the detection-period ancillary control function, where RLD is the rate limiter dynamic in order to avoid the power output pulse of the HVdc ancillary control, $f_{\rm local}$ is the operating frequency monitored at PCC bus of power control terminal, $f_{\rm remote}$ is the operating frequency monitored at PCC bus of dc voltage control terminal, the $f_{\rm ref}$ is the nominal frequency, $f_{\rm deadband}$ is the dead-band of the FPDC, K_f is the droop coefficient of the detection-period ancillary control and $\Delta P_{\rm dc}$ is power adjustment of the HVdc intertie.

to provide fast and adequate ancillary control after getting the results from HDD. Therefore, the insufficient response caused by DACF and the influence caused by FDIAs can be compensated. The general control diagram of the HDD-based HVdc ancillary control is shown in Fig. 4.

As shown in Fig. 4, the CRCF could be divided into two stages. Five types of cyber-attack methods (A1–A5) can lead to three potential wrong operating situations of the ancillary control of the HVdc intertie—trigging fake event, burying real event, and inverse response event. Thus, the stage I of the CRCF is to classify the detected FDIA into three types. This distinction is based on the following considerations.

- The A1, A3, A4, and A5 can fake an event to the HVdc intertie, such as a generator trip event or load trip event, and the HVdc intertie may be misleading and provide wrong ancillary control to its connected ac system.
- 2) Besides the triggering fake event, the A3 can also cause HVdc intertie to not provide the ancillary control during the system event.
- 3) Among the attack methods A1–A5, the A2 has the worst potential effect on the ancillary control of the HVdc intertie. The A2 may lead to the HVdc intertie provide inverse ancillary control to the disturbed system, which further exacerbates the influence of the system event.

After that, stage II of the CRCF is to develop four control strategies for suppressing the influence caused by the FDIAs, including normal strategy, the fake event recovering strategy, fast recovering strategy, and fast reversal strategy.

The normal strategy is designed for the ancillary control adjustment of the HVdc intertie when no FDIA happens based on the results from HDD. Due to the control delay caused by HDD calculation, the CRCF needs to compensate for the response insufficient of DACF. The normal operation strategy of the CRCF is to adjust the K_f of DACF to K_{normal} to provide faster ancillary control, so that similar ancillary control performance with conventional HVdc ancillary control can be realized.

The fake event recovering strategy aims at recovering the wrong ancillary control. When the label L_A of FDIAs is detected by HDD and the HVdc intertie has been providing ancillary control to the system due to the fake event signal, the fake event

recovering strategy is activated. The control process of the fake event recovering strategy can be described as follows.

- 1) *Step* 1: The CRCF sends a signal to the *hold* function in the DACF to stop the further ancillary control from the DACF.
- Step 2: The CRCF adjusts the current power flow of the HVdc intertie back to the original power flow.
- 3) *Step* 3: Release the *hold* function in DACF in order to eliminate the influence caused by the cyberattack.

To compensate for the response blocking of the HVdc intertie during the event, the objective of the fast recovering strategy is to provide the fastest response using the HVdc intertie in the disturbed system. When the FDIA is detected by HDD and the buried real event is redetected at t_f , the CRCF adjusts the K_f of DACF to K_{f-max} so that providing ancillary support to the disturbed system as much as possible.

The fast reversal strategy is developed to mitigate the effect of the inverse response event. When the cyber attack is detected by HDD and the HVdc intertie is providing the inverse ancillary control, the control process of the fast reversal strategy could be described as follows.

- 1) *Step* 1: The fast reversal strategy of the CRCF exchanges the measured data from PMUs in the DACF.
- 2) Step 2: The CRCF adjusts the K_f of DACF to $K_{f-\max}$ so that providing correct ancillary control with the maximum response rate. The disturbance caused by the event and inverse HVdc ancillary control can then be suppressed until the end time of the cyberattack.

VI. EXPERIMENTS AND ANALYSIS

A. Comparison of Different Attack Intensity and Duration Time

To verify the control performance of HVdc under cyberattack, two types of experiments are conducted, including the HDD-based FDIA detection and HDD-based HVdc control performance experiments.

The first experiment is to verify the performance of the HDD. Specifically, the high-speed sampling frequency data with 1440 Hz reporting rate is collected in the Eastern Interconnection system. Totally 18 000 samples are used, wherein the length of each sample is 4500. All the samples are divided into three parts including the training, verification and test set. Moreover, reserved PMU data that did not participate in training is used to verify the data exchange attack. In HDD, the parameters of MSVM is optimized by the particle swarm optimization (PSO) method. To achieve real-time detection, the input frequency data is downsampled to 500 points per sample and then the feature F_2 is downsampled to 25 points.

The characteristics of the attack determine the response behavior of the HVdc control system. In this section, two characteristics including attack intensity and duration time are considered to verify the effectiveness of the HVdc control. The kernels have a great impact on the MSVM, thus different kernels are first evaluated. The attack identification results under some commonly used kernels are listed in Table II.



Fig. 4. Control diagram of the proposed HDD-based HVdc ancillary control, where K_{normal} is the droop coefficient of CRCF normal strategy, P_{op} is operating power flow of the HVdc intertie, P_{origin} is the power flow of the HVdc intertie before the cyberattack, the ΔP_{TFE} is power output of the CRCF fake event recovering strategy, the K_{f-max} is the maximum droop coefficient of the HDD-based HVdc ancillary control.

TABLE II PERFORMANCE USING DIFFERENT MULTIKERNEL SVM

	Multi-ker	Accuracy (%)		
k_1	k_2	k_3	k_4	Accuracy (70)
Lin	Lin	Lin	Lin	92.03
Pol	Pol	Pol	Pol	93.85
Pol	Pol	Lin	Lin	93.72
RBF	RBF	RBF	RBF	94.28
Pol	Pol	RBF	RBF	94.98

TABLE III PERFORMANCE COMPARISON UNDER DIFFERENT ATTACK INTENSITY

Models	1	Test		
Widdels	50 mHz	100 mHz	200 mHz	time (ms)
HHT(EEMD)-MSVM	86.85	86.78	87.09	40.16
HHT(VMD)-SVM	92.71	92.18	92.56	37.75
HHT(EWT)-MSVM	83.11	85.28	84.92	0.75
$HHT_1(VMD-no F_1)-MSVM$	94.17	93.56	94.43	38.46
HHT(VMD)-MSVM	95.30	94.19	95.45	38.73

It is worth noting that the optimized weight w_d and kernel parameters are used in the following experiments. The bold entities mean that this method has higher accuracy. It can be seen from Table II that the HDD obtains the lowest accuracy when all the kernels are linear kernels. Generally, the RBF kernels have better performance. After combining the polynomial and RBF kernels, 94.73% accuracy is achieved. Finally, the weights w_d are set to {0.0561, 0.4430, 0.3450, 0.1559}. The kernel parameters of k_d are set to {6.0820, 2.8592, 8.5668, 0.9307}. Meanwhile, the number of BMFs is set to t = 6 using the grid search method.

Next, different attack intensity experiments are used to verify the results of the proposed HHT-MSVM. The attack intensity means the amplitude difference before and after the attack. The empirical wavelet transform (EWT) [34] and EEMD are used to compare with the VMD. The attack identification results are listed in Table III. The test time is the running time per sample. To verify the effectiveness of this feature F_1 , an experiment that does not contain this feature is conducted. The HHT₁ denotes the weight of F_1 is assigned to F_3 (weight of F_3 becomes $w_3 =$ 0.4011). The other parameters of HHT₁(VMD-no F_1)-MSVM are the same as the HHT(VMD)-MSVM. For a fair comparison and to ensure that the dimensions of features are consistent, the number of decomposition is set to 6 for both EWT and EEMD. The vector of initial bounds is set to [2, 50, 100, 150, 200] where the adaptive regression method is used in the EWT method.

Compared with HHT-MSVM using EEMD and VMD, the results show that the HHT with VMD performs better than the EEMD method. This is because the VMD is suitable for the decomposition of nonlinear signals. Compared to EWT and VMD methods, it can be seen that the results of EWT-MSVM is about 85% and is lower than the accuracy of EEMD and VMD under different attack intensity. This reason is that the filter is used in EWT, and the filter causes energy leakage, which introduces decomposition errors. The other reason is that the features extracted by the decomposition result of EWT are not distinguishable. Additionally, the proposed MSVM obtains about 2.7% higher than the SVM method under different attack intensities. Compared with $HHT_1(VMD-no F_1)-MSVM$ and HHT(VMD)-MSVM, the results show that the F_1 contributes to the improvement of identification accuracy for FDIAs, which more than 1% accuracy can be improved. The EEMD-MSVM consumes more time than because the EEMD is derived from multiple EMD calculations. However, the detected speed of EWT is the fastest one, which indicates the EWT has the best real-time performance. Meanwhile, the running time of MSVM is similar to SVM, which indicates the effectiveness of MSVM.

To compare the performance of MSVM with other classifiers, three types of commonly used classifier methods are selected, including the random forest (RF) and artificial neural network (ANN), and ridge classifiers [35]. For the RF, the max depth parameter is optimized set to 12 from [1, 15]. For ANN, the structure is set as 43-40-30-6, which the 43 and 6 denotes the input features and output classes, respectively. The number of hidden nodes are 40 and 30, which is selected through trial and error. The regularization strength parameter of the Ridge classifier is set to 0.5 through the grid search method. The F_1 and F_4 features are connected together and fed into different classifier methods. The identification results are listed in Table IV.

As shown in Table IV, it can be seen that the ANN and Ridge have similar performance and test time for FDIAs. The maximum 86.11% is obtained for ANN and Ridge classifiers, indicating the weaker ability to identify different FDIAs. The highest performance of RF is 95.13%, which means that the RF has high recognition ability. Compared with RF and MSVM, the MSVM obtains 1% higher accuracy than the RF with 100 mHz attack intensity. This means that the MSVM has better

Models		Test		
Widdels	50 mHz	100 mHz	200 mHz	time (ms)
HHT(VMD)-RF	94.90	93.14	95.13	38.49
HHT(VMD)-ANN	80.04	80.31	82.94	38.48
HHT(VMD)-Ridge	85.92	85.35	86.11	38.48
HHT(VMD)-MSVM	95.30	94.19	95.45	38.73

TABLE IV PERFORMANCE COMPARISON UNDER DIFFERENT CLASSIFICATION METHODS



Fig. 5. Comparison of three duration time detection methods.

TABLE V PERFORMANCE COMPARISON USING DIFFERENT DETECTION METHODS

Models -	Accuracy (%)			$M\Lambda(\mathcal{O}_{0})$	Test
	30%	40%	50%	MA(n)	time (ms)
CNN	75.96	76.59	81.66	-	9×10^{-2}
CNN-LSTM	83.36	84.15	85.44	-	0.23
WT-FFT-ANN	74.51	74.93	76.22	-	5.35
HDD	94.61	94.89	95.30	84.58	57.78

-: the method cannot achieve duration time detection.

performance because multiple kernel functions are used. The detection time of different methods is less than 39 ms, which reveals that these classifiers have similar computational efficiency. Overall, the effectiveness of the proposed HHT-MSVM is verified compared with different decomposition methods and classifiers.

To compare the accuracy of the k-Means and some advanced unsupervised method, the duration time result is further carried out as presented in Fig. 5. The local outlier factor (LOF) and histogram-based outlier detection (HBOS) are two fast outlier detection methods [36]. In these three methods, the cluster parameter can be set as 2 to classify the normal and attack data points. The minkowski distance is used in LOF. And the amount of contamination is set to 0.1 in LOF and HBOS. The results depict that the k-Means has higher MA values than the LOF and HBOS for A1-A4. The accuracy of A5 is relatively low is because the attacked data value is very closed to the normal data. Eventually, most of the cyberattack methods can be detected by the k-Means.

B. Comparison With State-of-the-Art Methods

Thereafter, three advanced frequency data detection methods are used to further verify the performance under different ratios of training data, as shown in Table V. These methods include the convolutional neural network (CNN), long short-term memory

TABLE VI PERFORMANCE COMPARISON USING STATE-OF-THE-ART METHODS

Models	Accuracy (%) 30% 40% 50%		MA(%)	Test time (ms)	
MM-RFC	72.86	73.02	73.58	-	1.93
MM-gcForest	63.45	64.24	66.47	-	2.21
EEMD-FFT-BP	72.84	73.07	72.95	-	43.25
HDD	94.61	94.89	95.30	84.58	57.78

-: the method cannot achieve duration time detection.

(CNN-LSTM), and WT-FFT-ANN [22]. Both CNN and CNN-LSTM can automatically extract and identify the features from the input frequency data. It demonstrates that the accuracy of CNN and WT-FFT-ANN are the lowest, which only 76.22% and 81.66% accuracy are obtained under 50% training data. This is because CNN does not learn frequency domain features due to limited input information. The HDD has a profound performance in which 94.61% accuracy is reached even with 30% training data. Compared with WT-FFT-ANN, the HDD receives rich input features, including frequency and statistical domain. The test time of HDD is 57.78 ms, which the real-time HVdc ancillary control can be satisfied.

To compare the proposed method with some state-of-the-art FDIA detection methods, three state-of-the-art methods are selected, including the mathematical morphology (MM)-random forest classification (RFC) [37], MM-gcForest [23], and EEMD, fast Fourier transform (FFT), and back propagation (BP) neural network [38]. For the method MM-RFC [37] and MMgcForest [23], the MM is used to decompose the extracted FDIAs, and then 62 features (sparsity trends and sparsity roughness features) are extracted from the results of MM. Finally, the feature is fed into the classifiers. For the EEMD-FFT-BP method, all the frequency domain features are extracted using EEMD and FFT. To achieve a fair comparison, the number of decomposed intrinsic mode functions of EEMD is also set to 6. The length of the input features for BP is 1500. A two-layer BP is used and the nodes of each layer are 200 and 100. The detection results are listed in Table VI.

It can be seen that the MM-gcForest and MM-RFC obtain lower accuracy, where the accuracies under different training data are less than 75%. However, the EEMD-FFT-BP has a higher performance than the MM-RFC. This reason is that more features are retained (1500 points for the input). However, only two types of features are extracted for MM-gcForest and MM-RFC, which means these two types of features can not fully characterize the FDIAs. The HDD obtains higher accuracy because four features from the frequency domain and statistics domain are used. This means more information is retained. However, the test time of HDD is also longer because it takes time to determine the attack time and VMD calculation.

The message authentication scheme (MAC) is mainly used in the advanced metering infrastructure (AMI), where the smart meters are the primary devices [39]. MAC also has the potential to be used in a wide-area measurement system (WAMS). To compare the proposed HDD with the lightweight MAC method, the comparison results from the security perspective and computational requirements' perspective can be summarized in Table VII.

TABLE VII
COMPARISON OF THE PROPOSED HDD AND MAC METHODS

Index	HDD	MAC
Security for FDIA	Protected	Protected
Computational	57 78ms	2000-bit, 180us [40]
requirements	57.761118	200000-bit, 3000us [41]
	CPU needed(real time)	CPU needed(real time) [41]
Differences	Based on IEEE C37.118,	Hash-based MAC
	IEC 61850-90-5 [42]	or A-codes [40]
191 bus		이어 어리 528

Fig. 6. Configuration of the two-terminal VSC-HVdc intertie system.

rconnection

It can be seen that both the proposed HDD and MAC can defend the FDIA. For the MAC, take the Hash-based message authentication code (HMAC) as an example, if the message length is 2000-b, the worst execution time of HMAC is about 180 us [40]. And when the number of message length increases to 200 000 b, the verification delay would be 3000 us [41]. It can be seen that the proposed HDD and MAC can meet the real-time performance, where the MAC is faster.

It should be noted that there are some differences between the HDD method and MAC. The data-driven HDD method is based on the vulnerability of the transmission protocol. The purpose is to detect attack class and the duration time once the attack has occurred. The MAC is a type of cryptographic protection scheme, where its main purpose is to prevent being attacked. The HDD method can be deployed in a data server of the WAMS or a controller center. The MAC requires the meter to generate the key and encapsulate it in the security header of the data packet [43]. Therefore, the MAC needs to deploy distributed at each measuring device and data server.

C. Verification of HDD-Based HVDC Ancillary Control

The second experiment is to verify the effectiveness of the proposed HDD-based HVdc ancillary control. An integrated North American power system model is developed in PSCAD simulation software by combining the highly reduced models of EI and WECC, using two-terminal VSC-HVdc intertie between the interconnections, as shown in Fig. 6.

The highly reduced models of EI and WECC are developed based on a reduced equivalent system, which the data can be found in [44]. This reduced equivalent system has been verified from the frequency response perspective. To realize the effective PSCAD modeling, highly reduced models of EI and WECC are redesigned using the 7 and 6 dynamic cluster generations, which is further reduced from the reduced equivalent system in [44]. The corresponding dynamic cluster generation capacity can be referred to [4]. Combined with Fig. 6, the terminal I works at constant dc voltage control, and the terminal II works at constant power control in HVdc intertie. The HDD-based

TABLE VIII QUANTIFY COMPARISON OF HVDC FREQUENCY REGULATION CONTROL

Index	A1		A3		A2	
	No HDD	HDD	No HDD	HDD	No HDD	HDD
D_l	0.129	0.081	0.226	0.121	0.167	0.163
D_a	0.073	0.012	0.105	0.017	0.063	0.006

HVdc ancillary control is configured on the terminal I to provide power support under the event.

In this section, three different types of FDIA are selected to verify the performance of the proposed HDD-based HVdc ancillary control including A1, A2, and A3. Fig. 7 shows the performance of HDD-based HVdc ancillary control under different types of frequency data attacks. Here, the ramping attack case is taken as an example and described in detail. It is notable that the purple star in Fig. 7 denotes the t_f .

As shown in Fig. 7(a1), a ramping attack with 0.7 Hz magnitude injects into PMU1 at 3 s. The HVdc intertie detects the frequency change from the PMU1 data and starts to activate its HVdc ancillary control. The power flow on the HVdc intertie from the PMU1 system to the PMU2 system is increased. Due to the power injection from the HVdc intertie, the frequency in the PMU2 system is also increased.

Without HDD-based HVdc ancillary control, due to the wrong response from the traditional HVdc ancillary control, the frequency measurement of PMU2 continuously deviates from the normal frequency as shown in Fig. 7(a2) and 7(a3). The largest frequency deviation of PMU2 reached 0.20 Hz. The power flow on the HVdc intertie also deviates from scheduled power, the largest power deviation is about 350 MW. However, with the HDD-based HVdc ancillary control, the FDIA is detected at $t_f = 5.1 s$, then the label L_A and t_f is sent to CRCF of HVdc intertie. The CRCF is activated and an appropriate control strategy is selected according to label L_A . Then the control signal is sent from CRCF to DACF and the power flow from PMU1 system to PMU2 system is deceased speedily. In Fig. 7(a2) and (a3), it shows that the frequency and power flow start to recover and back to the nominal value under the HDD-based HVdc ancillary control. From the solid blue line of Fig. 7(a2)and (a3), the largest frequency deviation and power deviations are less than 0.02 Hz and 50 MW, which has been significantly reduced after 13 s.

Combing the results from Fig. 7(b) and (c), it is clearly found that the HDD-based HVdc ancillary control could significantly suppress the frequency oscillation and realize the fast frequency recovery. Besides, to quantify the HVdc ancillary control performance, two indexes, including the largest frequency deviation (D_l) and average frequency deviation (D_a) are adopted as the indexes to quantify the HVdc ancillary control performance. Table VIII shows the quantified comparison of the HVdc ancillary control performance with and without the HDD methods. As shown in Table VIII, both the largest frequency deviation and average frequency deviation of the HVdc ancillary control with HDD is much smaller than the HVdc ancillary control without HDD. Overall, the performance of HDD-based HVdc ancillary control indicates that the active power and frequency are adjusted accurately and in time.



Fig. 7. Simulation result of the HDD-based HVdc ancillary control under different cyberattacks. (a1)–(a3) Ramping attack during [3 s, 7s], $t_f = 5.1 s$. (b1)–(b3) Data loss attack during [3 s, 7s], $t_f = 3.8 s$. (c1)–(c3) Data exchange attack during [3 s, 20s], $t_f = 3.9 s$.

VII. CONCLUSION

In this article, a hybrid data-driven based HVdc ancillary control strategy was proposed to detect and suppress the effects of FDIAs on the HVdc intertie operating stability. The attacked frequency type and duration time were quickly identified using the HDD. Different multikernels tests indicated that the MSVM can fully obtain the attack feature information using the real-world frequency data. The identification experiments with different attack intensity showed that the proposed HHT and MSVM could effectively identify five types of frequency attacks. The simulation verification of the HDD-based HVdc ancillary control was demonstrated based on a highly reduced power system model of the three North American interconnections. Based on the detected identification and duration time information in HDD, different comparison results revealed that the proposed HDD-based HVdc ancillary control could significantly suppress the impact of the frequency attacks on the HVdc intertie operation. In the future work, the MAC-based cyber defense method can be the promising directions for our research.

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